Total No. of Questions: 11 ]

[ Total No. of Printed Pages : 3

# **DPG-2290**

# PG Diploma in Computer Application Examination, 2022

## **COMPUTER ORGANIZATION**

Paper - PGDCA-101

Time: 3 Hours [Maximum Marks: 50]

Section-A (Marks:  $2 \times 10 = 20$ )

Note: Answer all ten questions (Answer limit 50 words). Each question carries2 marks.

Section–B (Marks :  $3 \times 5 = 15$ )

Note: Answer all five questions. Each question has internal choice (Answer limit200 words). Each question carries 3 marks.

Section–C (Marks:  $5 \times 3 = 15$ )

**Note**: Answer any *three* questions out of five (Answer limit **500** words). Each question carries **5** marks.

#### Section-A

- 1. (i) What is 1's complement?
  - (ii) Define Overflow.

BR-251 (1) DPG-2290 P.T.O.

	What <b>R-25</b>	at is the use of cache memory in computer?	
	r	Or	
5.		ain memory hierarchy.	
	Expla	<i>Or</i> ain modes of data transfer.	
4.	схріа	ain any three peripheral devices.	
1	_	ain JK flip-flop.	
	Б. (	Or	
3.	Expla	ain De Morgan's Law.	
	What	at is the use of 2's complement in binary subtraction?	Explain with example.
		Or	
	(iii)	$(11110011101)_2 \rightarrow (?)_{16}$	
	(ii)	$(2D4)_{16} \rightarrow (?)_{8}$	
	(i)	$(4562)_{10} \rightarrow (?)_2$	
2.	Write	e down the following number system conversions :	
		Section-B	
	(x)	Define Word Size.	
	(ix)	What is the work of Address Bus?	
	(viii)	What is PROM ?	
	(vii)	Define static RAM.	
	(vi)	Define Asynchronous data transfer.	
	(v)	What is the role of I/O processor?	
	(iv)	What is the use of flip-flop?	
	(iii)	Draw 4 × 1 multiplexer diagram.	

6. Explain register set.

Or

Explain 8085 instruction format.

### Section-C

- 7. Explain booth's algorithm with example.
- 8. Solve using *k*-map:

$$F(a, b, c, d) = \Sigma(0, 2, 5, 7, 8, 10, 13, 15)$$

- 9. Explain data transfer using direct memory access.
- 10. Describe mapping in virtual memory in detail.
- 11. Explain different addressing modes.