

# UGP-295

## B.C.A. (Part-II) Examination, 2021

### COMPUTER ORGANIZATION

Paper - BCA 201

Time : 1½ Hours ]

[ Maximum Marks

#### Section-A

(Marks : 2 × 10 =

Instruction :- Answer all *ten* questions (Answer limit **50** words). Each question carries **2** marks.

#### Section-B

(Marks : 4 × 5 =

Instruction :- Answer all *five* questions. Each question has internal choice (Answer limit **200** words). Each question carries **4** marks.

#### Section-C

(Marks : 10 × 3 =

Instruction :- Answer any *three* questions out of five (Answer limit **500** words). Each question carries **10** marks.

#### Section-A

Attempt all *ten* questions. Answers should not exceed **50** words in each question.

- (i) Define Multiprocessing.

- (iv) Define RAM.
- (v) Draw the flow of Instruction Cycle.
- (vi) Suggest about Program Counter.
- (vii) What is Primary Storage Device ?
- (viii) What is Flip Flop ?
- (ix) What is DMA ?
- (x) Give the truth table of X-NOR Gate.

### **Section–B**

*e* :- Answer all *five* questions (Answer limit **200** words).

Write briefly about computer fundamental system.

*Or*

Explain Software Interaction.

What NAND Gate is called a universal gate ? Justify your answer.

*Or*

Consider a four variable Boolean function  $F = \Sigma (0, 4, 6, 7, 8, 10, 11, 15)$

Minimise this function using K-MAP.

Explain the Input/Output Processor.

*Or*

Explain DMA.

Explain the Cash Memory.

*Or*

(a) Stack Organisation

(b) Register

*Or*

Explain the Memory Reference Instruction.

### **Section–C**

*te* :- Answer any *three* questions out of five (Answer limit **500** words).

What do you mean by Multiplexer ? Explain the different type of Multiplexer.

Explain the various modes of Data Transfer Technique.

Discuss Bit and Byte.

What is Addressing ? Discuss various addressing modes.

Discuss Combinational Curcuits.